

# Co-simulation of Distributed Embedded Real-time Control Systems



Embedded Systems  
INSTITUTE

Radboud Universiteit Nijmegen



**Marcel Verhoef**<sup>1</sup> **Peter Visser**<sup>2</sup> **Jozef Hooman**<sup>3</sup> **Jan Broenink**<sup>2</sup>

<sup>1</sup> Chess and Radboud University Nijmegen

<sup>2</sup> University of Twente, Dept EE-M-CS, Control Engineering Group

<sup>3</sup> Embedded Systems Institute and Radboud University Nijmegen

# Agenda

- Context and motivation
- Basic techniques: Bond-graphs and VDM++
- Case study : Water tank level controller
- Tool support and integrated operational semantics
- Results and conclusions
- Current and future work

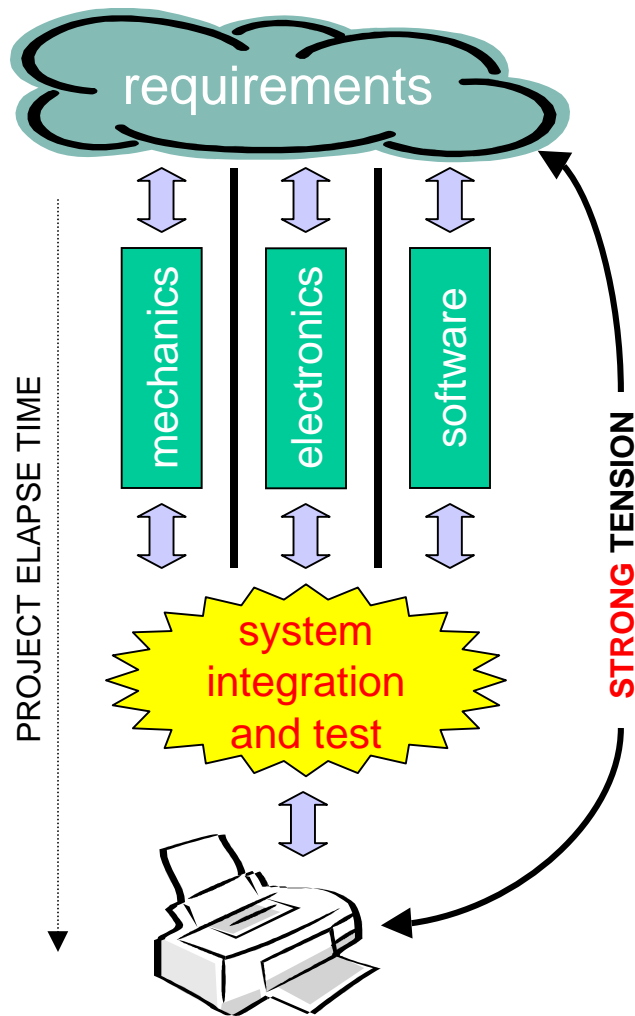
## Beyond the Ordinary: Design of Embedded Real-time Control

Bode-RC



- BODERC project @ ESI
- Sept 2002 - Apr 2007
- Multi-disciplinary design
  - mechanics
  - electronics
  - software
- High-tech systems focus
- Early life cycle trade-offs
- Industry as a laboratory
- <http://www.esi.nl/boderc>

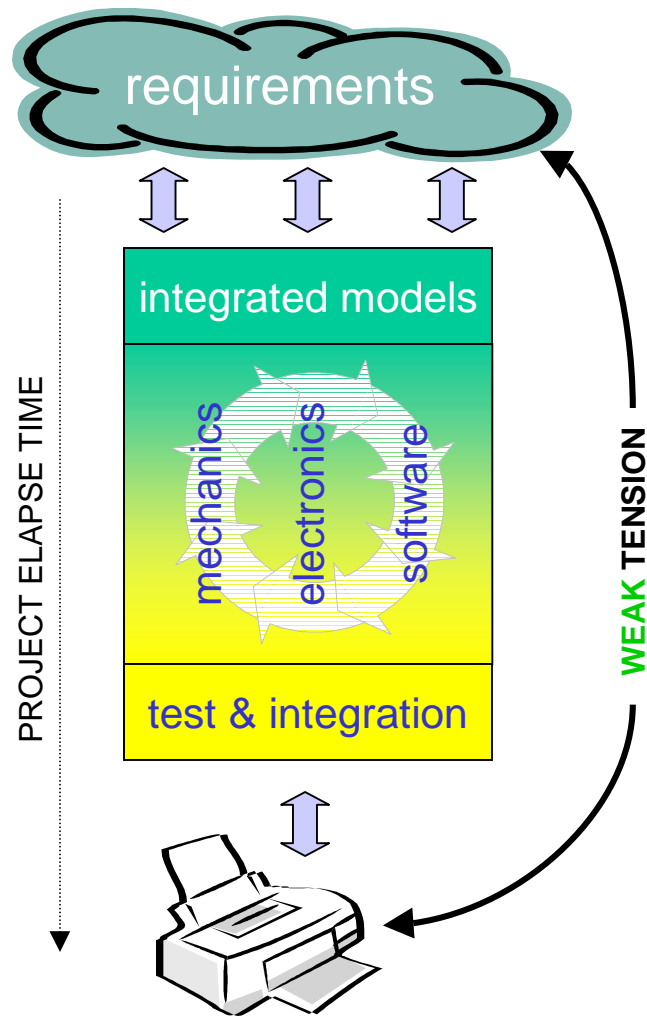
# Design of High-Tech Systems - State of Practice



- design is typically mono-disciplinary organised
- domain specific methods and custom tools are used
- out-of-phase development and system-level focus lacking
- cross-cutting concerns postponed to the integration phase
- late validation & feedback

**" INTEGRATION HELL "**

# Multi-disciplinary Systems Design - The Vision



- system level approach
- model-driven design
- integrated models & tools
- rapid evaluation
- early feedback
- support design dialogue
- continuous integration
- continuous validation
- less effort overall
- higher quality

# The Challenge - Integrated Design Models (1)

- Notations and analysis techniques used by the disciplines are fundamentally different
  - mechanics : finite element methods
  - electronics : differential or difference equations
  - software : labelled transition systems
- Is a common notation feasible\* at all?

\* [Henzinger & Sifakis, FM 2006 key note, LNCS 4085, pp 1-15]

## The Challenge - Integrated Design Models (2)

- scope of discipline specific tools is widening
  - Matlab Simulink → Stateflow, Real-Time Workshop, TrueTime
  - Rhapsody → Simulink
  - UML → SysML
- bigger piece of the pie ≠ satisfy all stakeholders
- **problems** : poor abstraction, restrictive MoCs
- novel actor-based techniques\* : Ptolemy-II
- **problems** : disruptive approach, poor semantics

\* [ <http://ptolemy.eecs.berkeley.edu> ]

# Our approach - Integrated Design Models (3)

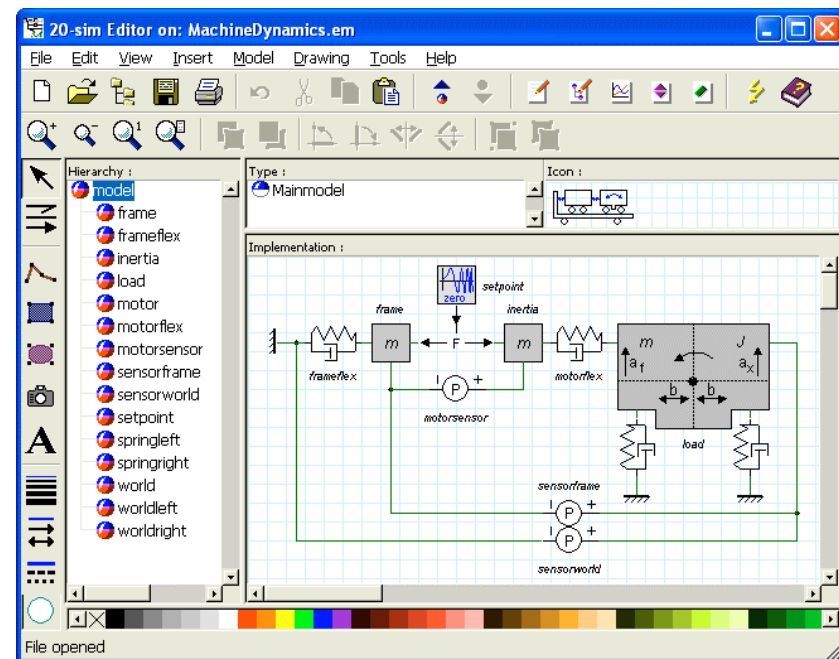
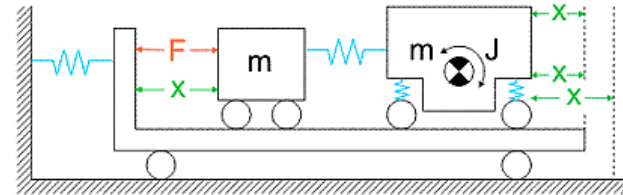
- Cross the continuous time - discrete event divide
- Select a well-defined (formal) notation on either side
- Explore semantic integration of those notations
- Implement tool support for these extensions
- Investigate models by (reliable) co-simulation
- Expected **benefits**:
  - good abstraction facilities on both sides of the divide
  - supports light-weight modelling required in early stages
  - few a-priori MoC specific restrictions → avoid design bias
  - fits in design flow → low acceptance threshold for industrial uptake



# Continuous Time Realm - Bond Graphs

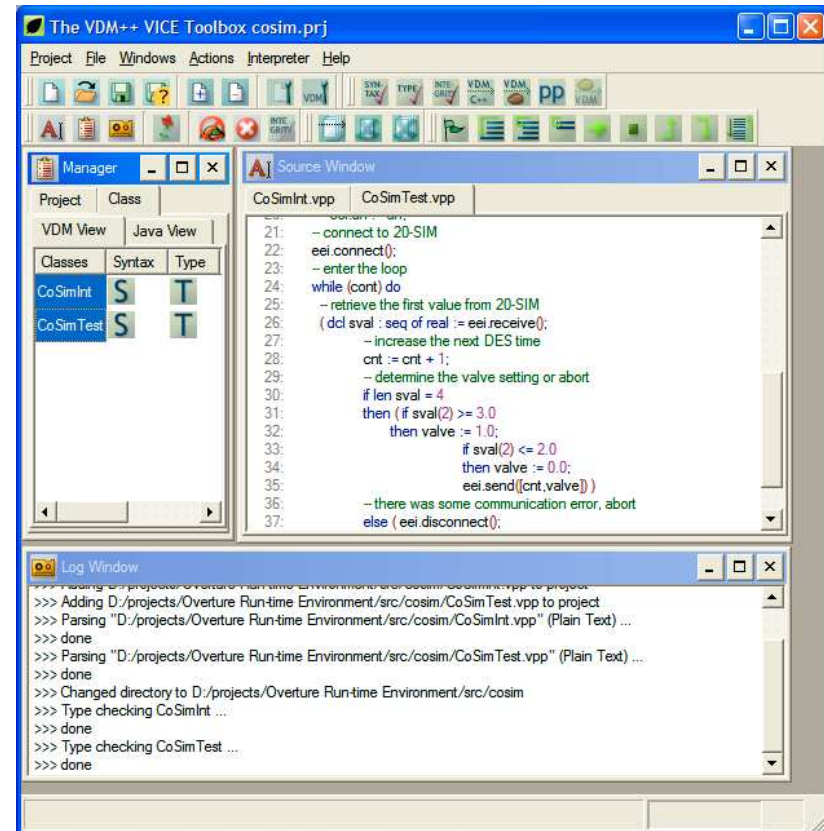
- dynamic systems modelling, physics domain independent
  - mechanics
  - electronics
  - pneumatics
- graphical notation: Bond graphs\*
- formal analysis for algebraic loops and differential causalities
- model validation through simulation and visualisation
- industry grade tool support  
<http://www.20sim.com>

\* [ Gawthrop, Bevan, IEEE Control Systems Magazine, April 2007, pp 24 - 45 ]



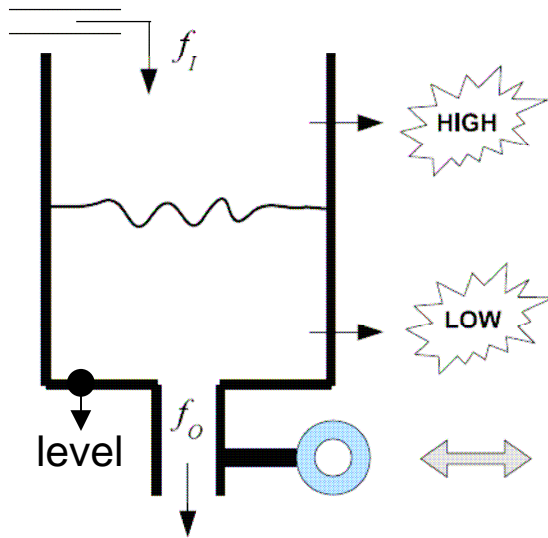
# Discrete Event Realm - VDM++

- object-oriented formal model-based specification language
- concurrency through threads
- round-trip engineering UML
- formal analysis of static and run-time (type) correctness
- model validation through prototyping & structured testing
- industrial grade tool support  
<http://www.vdmtools.jp/en>
- VICE extension\* for real time, scheduling and deployment

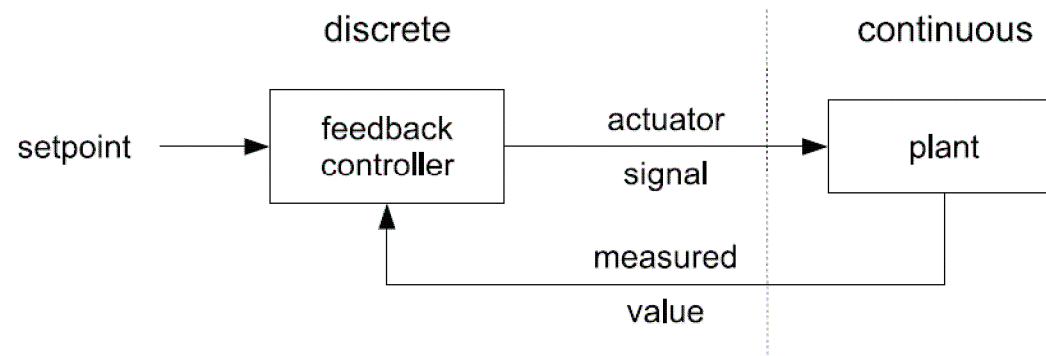


\* [ Verhoef, Larsen, Hooman, FM 2006, LNCS 4085, pp 145 - 162 ]

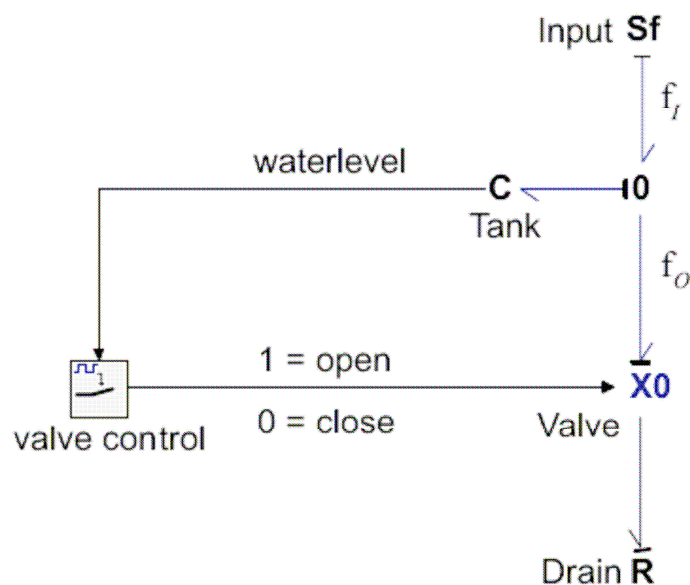
# Our Approach by Example - water tank case (1)



$$\frac{dV}{dt} = f_I - f_O$$
$$f_O = \begin{cases} \frac{\rho \cdot g}{A \cdot R} \cdot V & \text{if valve = open} \\ 0 & \text{if valve = closed} \end{cases}$$



# Our Approach by Example - water tank case (2)

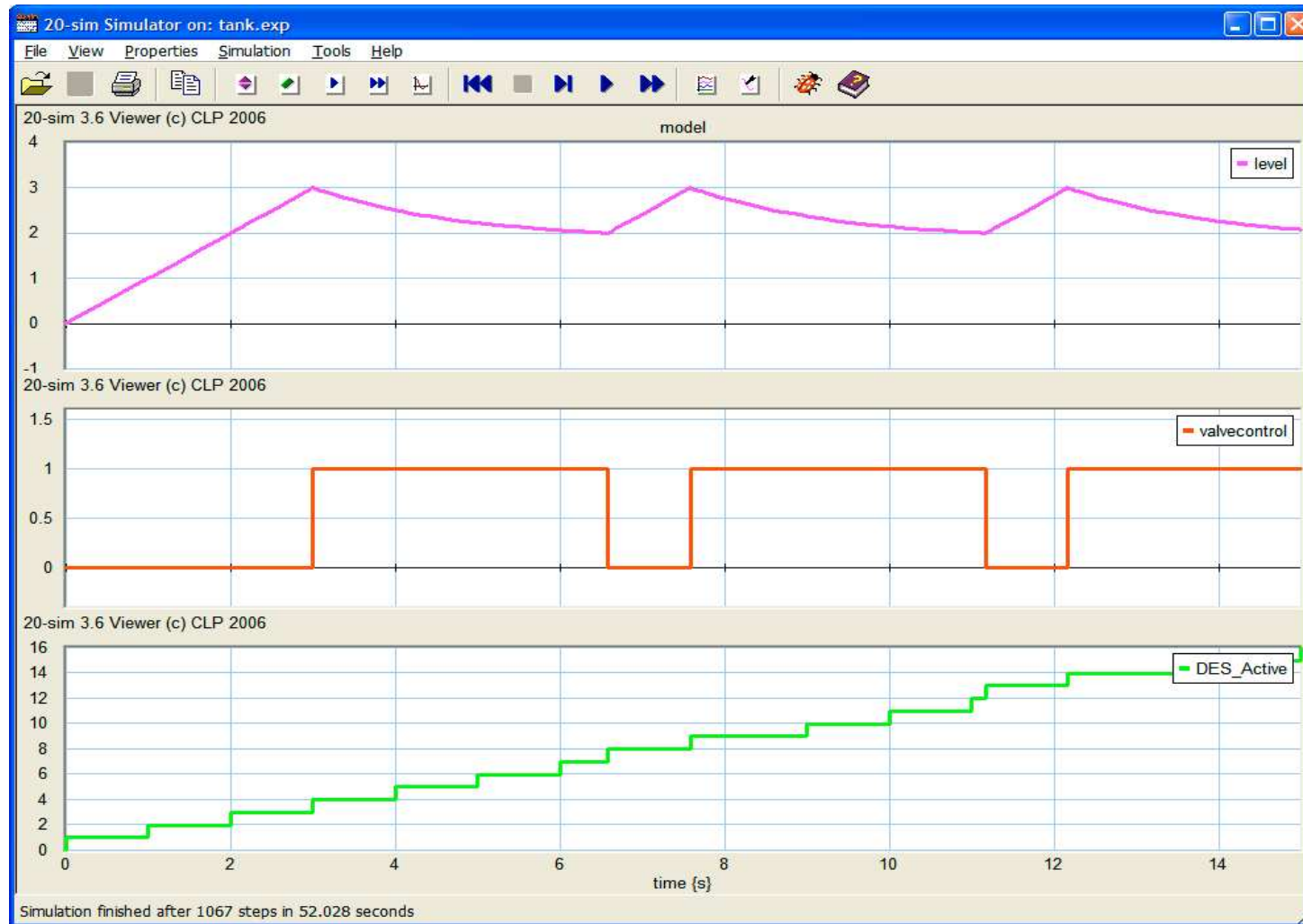


```
01 variables
02   real volume, level;
03 parameters
04   real area = 1.0;
05   real gravity = 9.81;
06   real density = 1.0;
07 equations
08 // p.e = pressure, p.f = flow rate
09 // integrate flow to obtain volume
10   volume = int(p.f);
11   level = volume / area;
12   p.e = gravity * level * density;
```

## Our Approach by Example - water tank case (3)

```
01 class Controller
02
03 instance variables
04   static public level : real;
05   static public valve : bool := false -- default is closed
06
07 operations
08   static public async open: () ==> ()
09   open () == duration(0.05) valve := true;
10
11   static public async close: () ==> ()
12   close () == cycles(1000) valve := false;
13
14   loop: () ==> ()
15   loop () ==
16     if level >= 3 then valve := true -- check high water mark
17     else if level <= 2 then valve := false; -- check low water mark
18
19 threads
20   periodic(1.0,0,0,1.0)(loop)
21
22 sync
23   mutex(open, close, loop)
24
25 end Controller
```

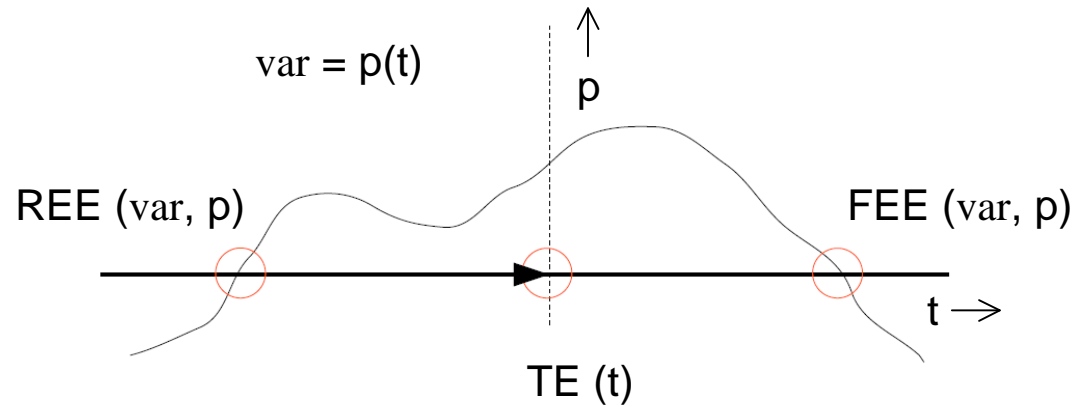
# Our Approach by Example - water tank case (4)



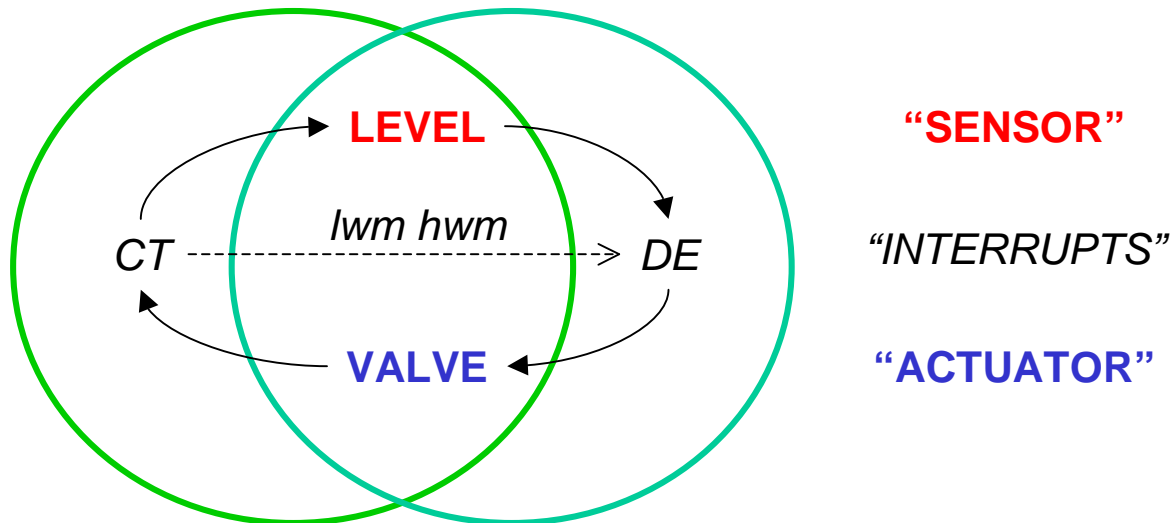
# Integrated Operational Semantics (1)

- Continuous Time model
  - sets of differential equations
  - approximate solution numerically
  - discrete integration over some time interval
  - many “solver” algorithms available e.g. Euler
  - CT **shares state variables** with DE model
  - capture **state events**: zero-crossing detection
  - capture **time events**: proceed to time  $t > \text{now}$

# Integrated Operational Semantics (2)



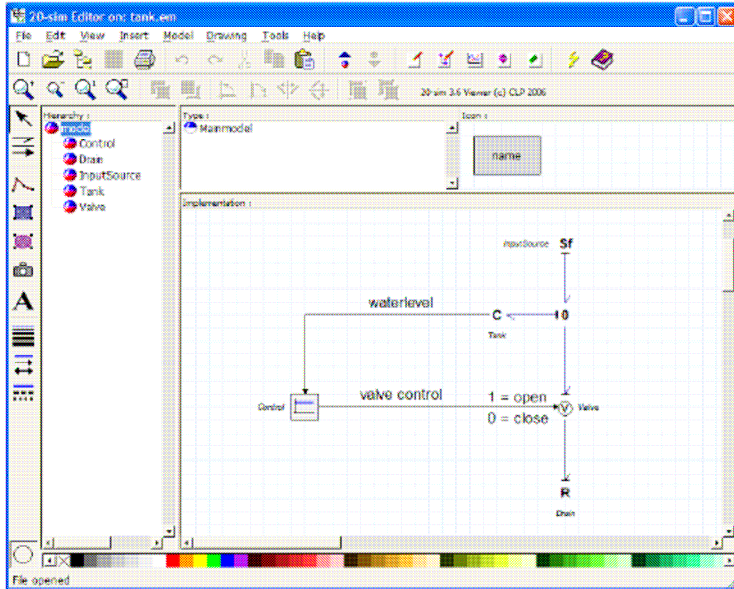
$$lwm = \text{FEE}(\text{level}, 2.0) \quad hwm = \text{REE}(\text{level}, 3.0)$$



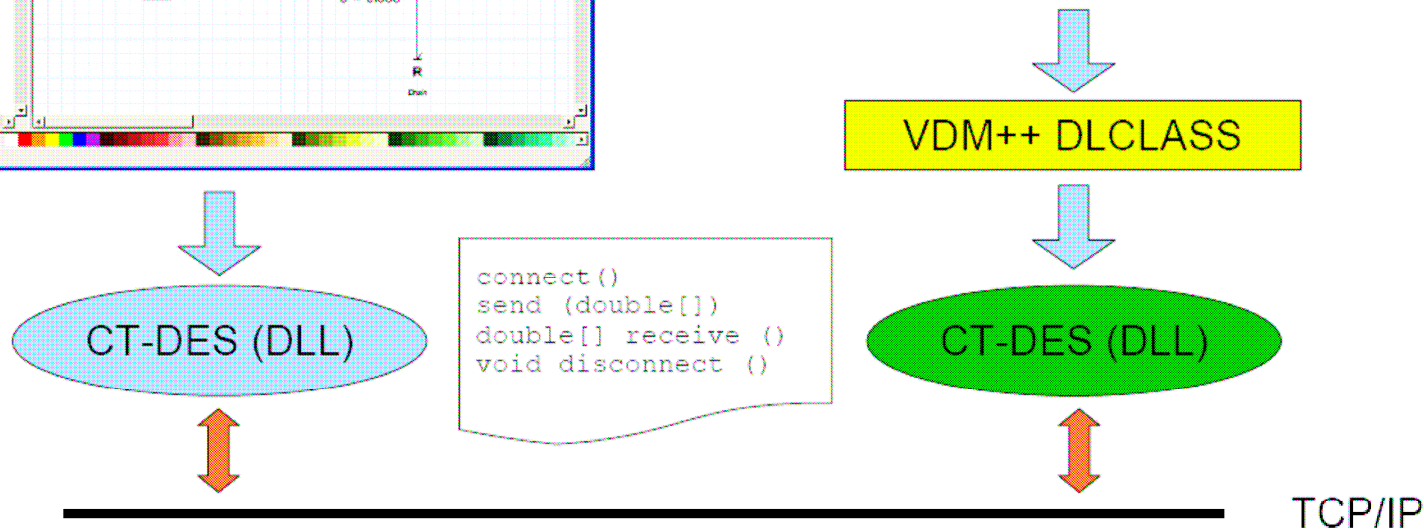
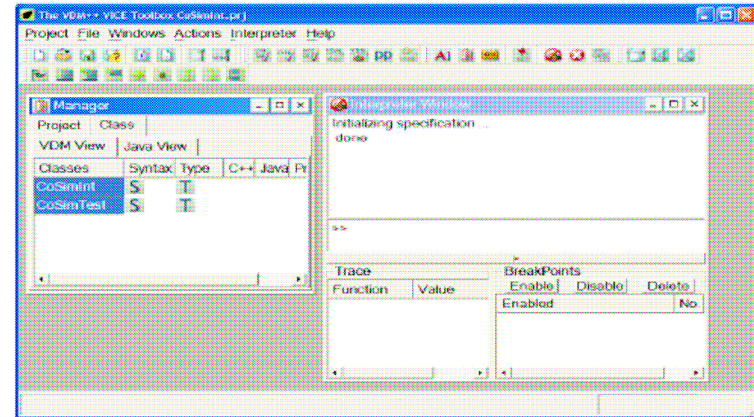


# Tool Support (1)

20-SIM (CT simulation)



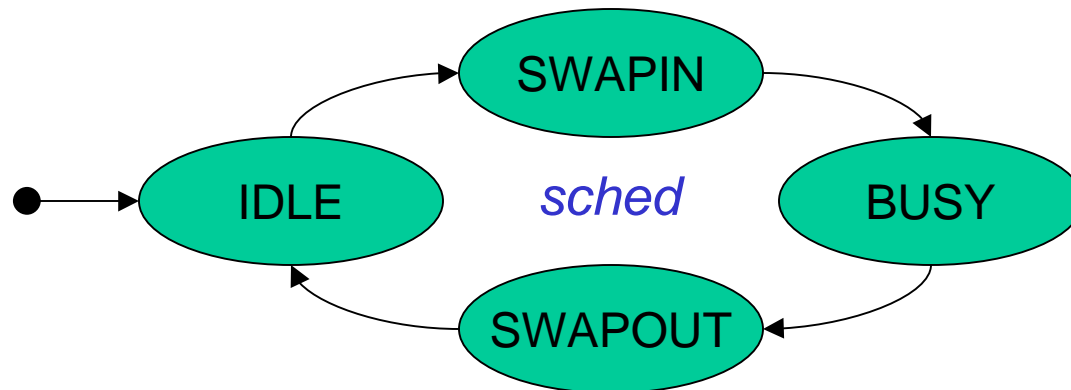
VDMTools (DE simulation)





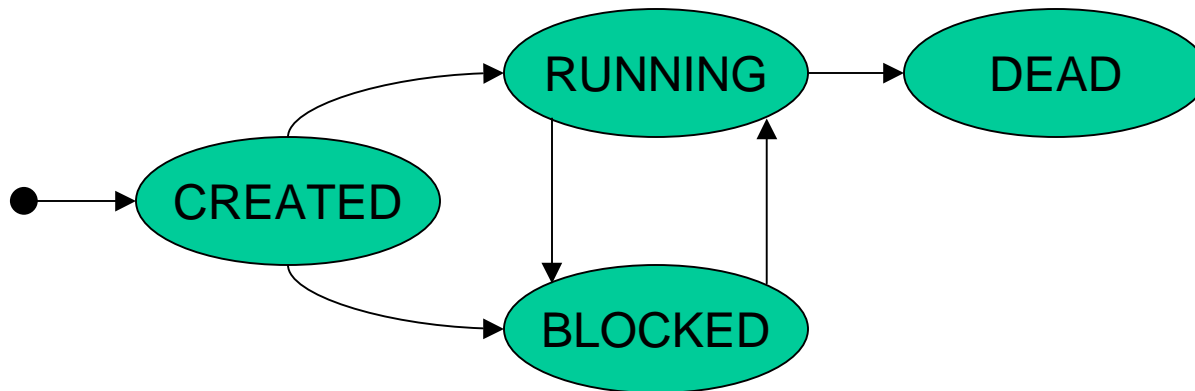
## Integrated Operational Semantics (3)

- Discrete Event model
  - assume given a set of resources  $R$   
 $\{ \text{cpu}_1, \text{cpu}_2, \text{cpu}_3, \text{bus}_1, \text{bus}_2 \}$
  - assume given an architecture  
 $\text{bus}_1 \rightarrow \{ \text{cpu}_1, \text{cpu}_2 \}, \text{bus}_2 \rightarrow \{ \text{cpu}_2, \text{cpu}_3 \}$
  - each resource has a scheduling state  $ss$



# Integrated Operational Semantics (4)

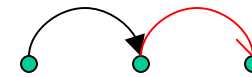
- Discrete Event model
  - each resource  $r \in R$  has a set of tasks  $r.T$  and an active task  $r.at \in r.T \vee r.at = \mathbf{nil}$ 
    - $cpu \rightarrow threads$
    - $bus \rightarrow messages$
  - each task  $t \in r.T$  has an execution state  $es$



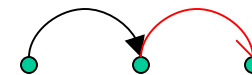
# Integrated Operational Semantics (5)

- Discrete Event model
  - each active task  $r.ta \neq \mathbf{nil}$  can
    - either execute a state transition
    - or execute a time transition

`x := 10`



`duration (100) x := 10`



`cycles (1000) (x := 10; y := 20)`



caveat: `duration (0)` is a valid time transition

## Integrated Operational Semantics (6)

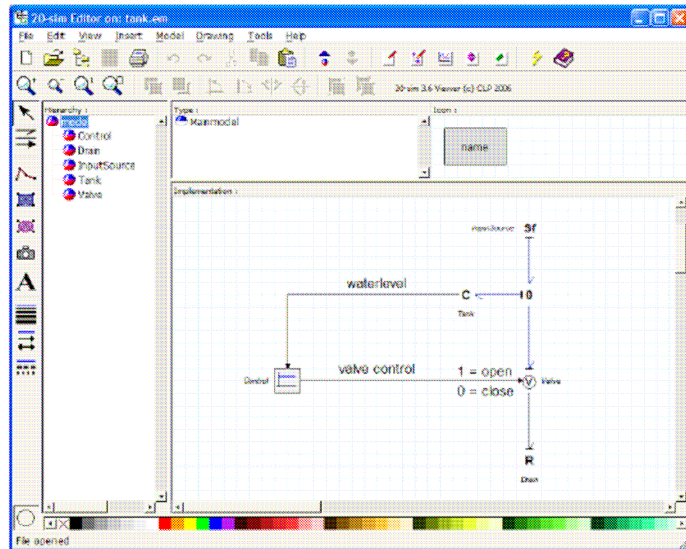
- process state transactions until all resources are either idle or need to make a time transition
- determine the smallest DE time step  $t_{req}$  over all R
- CT solver is asked to move to  $t + t_{req}$
- CT solver reaches  $t + t_{rel}$  with  $t_{rel} \leq t_{req}$
- time on all resources is updated to  $t + t_{rel}$
- events are handled (if any occurred)
- guards and scheduler are re-evaluated (if affected)
- repeat until abort time event is reached

## Results and conclusions

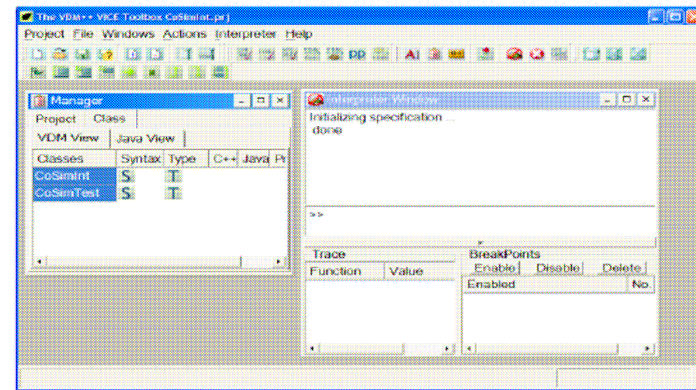
- Coupling does not restrict tools or add complexity
- Co-simulation enables cross-discipline dialogue
- Small model size due to abstraction on both sides
- Evaluation of design options requires low effort
- Discipline specific analysis on models is still feasible
- Generic integrated operational semantics
- Heterogeneous simulation is within reach
- Case studies: light-weight models can be accurate

# Future Work (1)

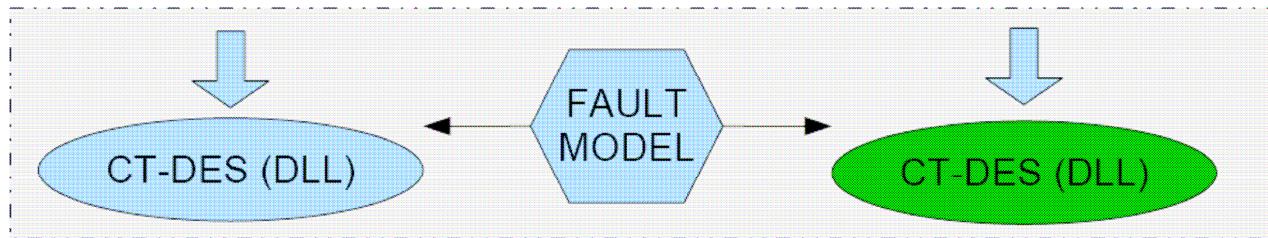
20-SIM (CT simulation)



VDMTools (DE simulation)



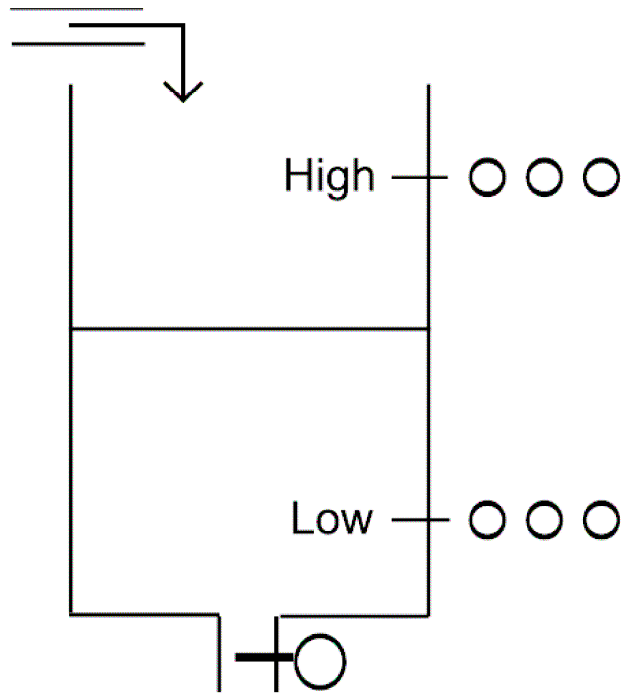
VDM++ DLCLASS



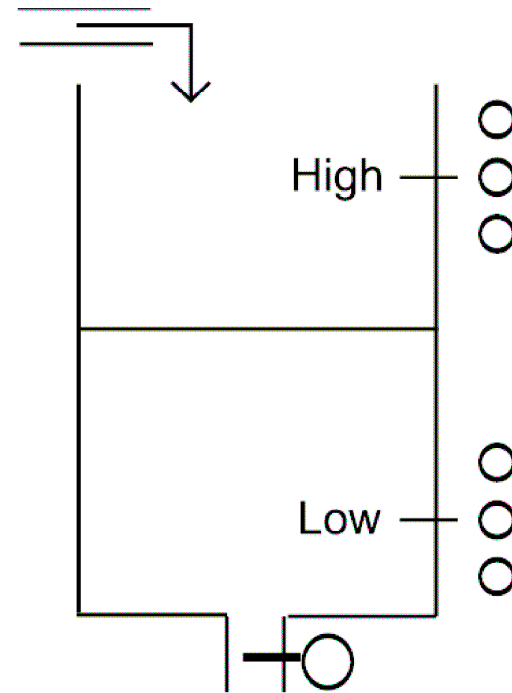
TCP/IP



## Future Work (2)



a: TMR approach



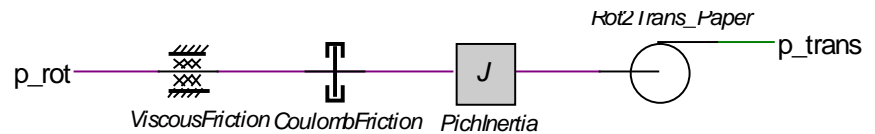
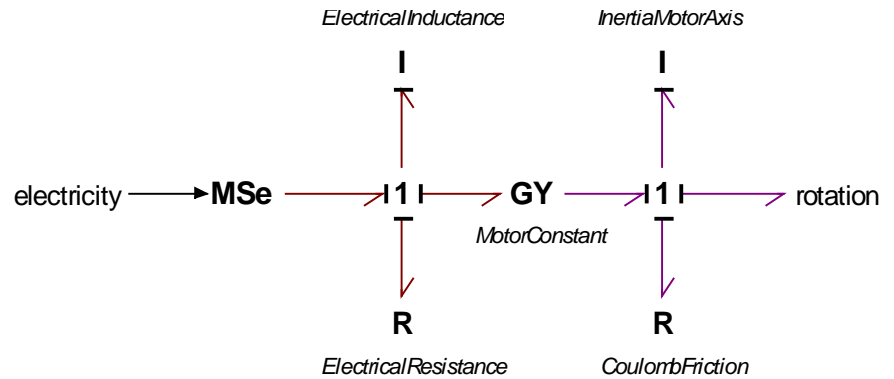
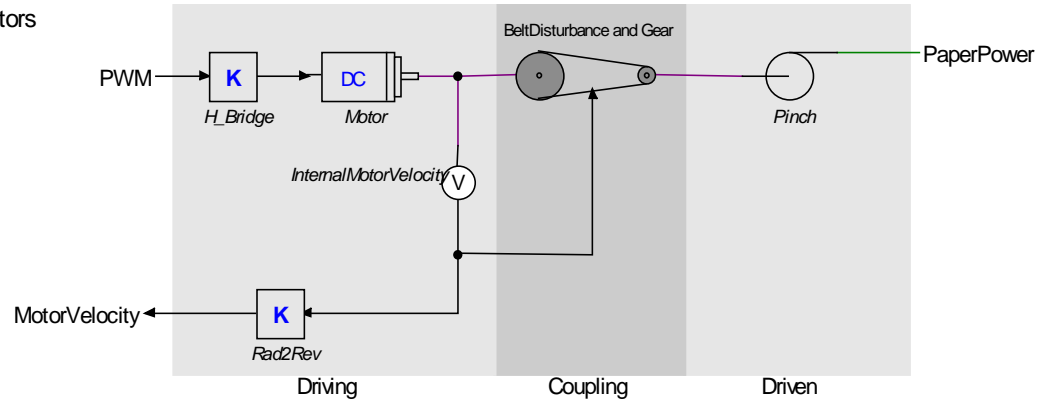
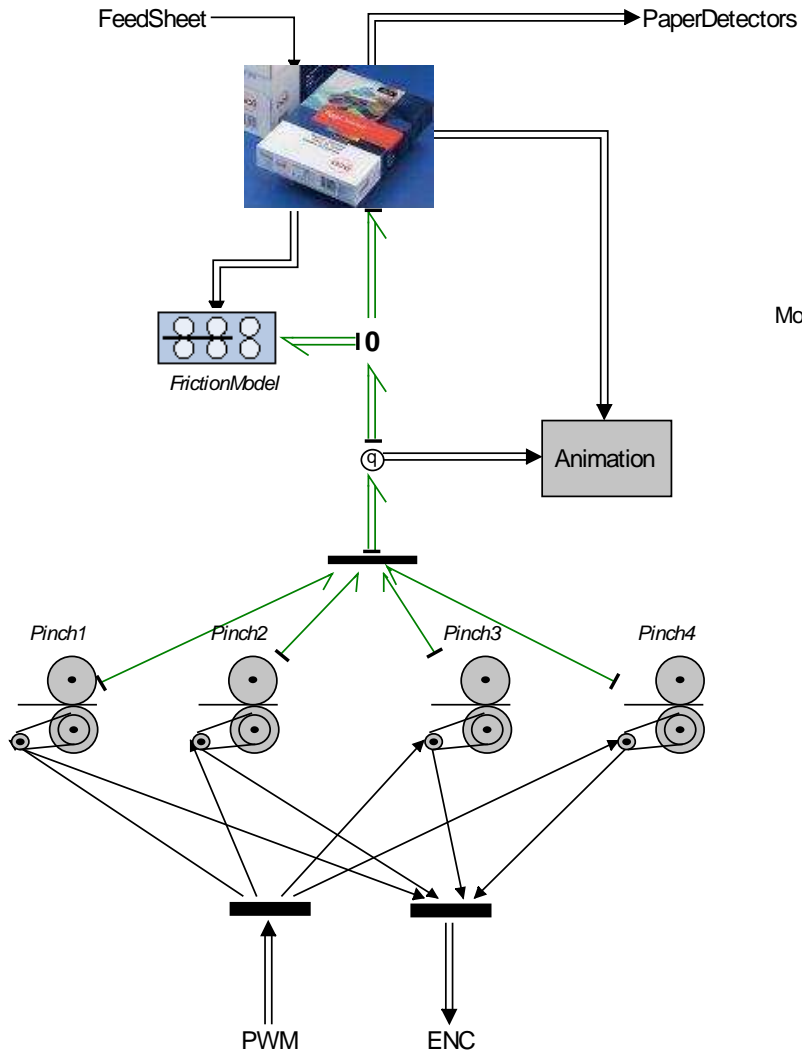
b: Linear approach

[ Andrews, Verhoef, Fitzgerald, DSN 2007 ]

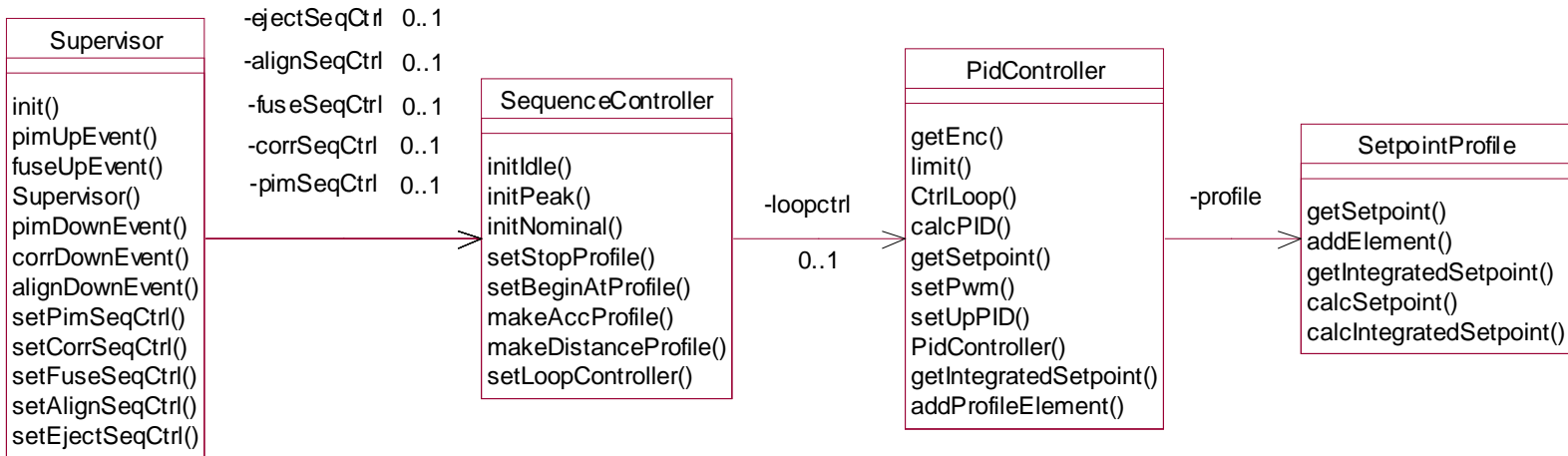
# Printer paper path - case study (1)



# Printer paper path - case study (2)



# Printer paper path - case study (3)

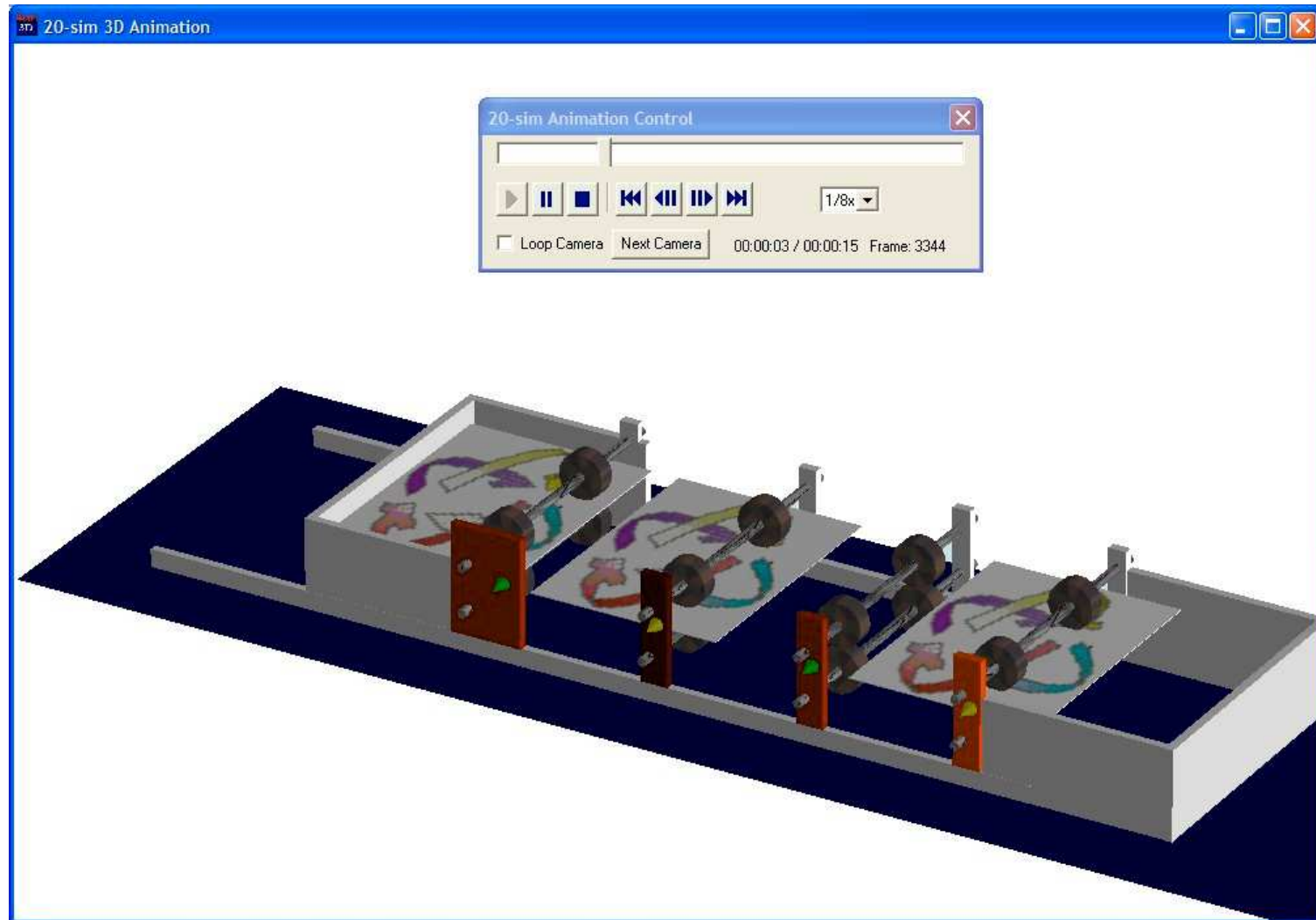


```

public CtrlLoop: () ==> ()
CtrlLoop () ==
    -- first retrieve the current encoder value
    ( dcl measured_value : real := ENCODER_GAIN * getEnc();
      -- output the previous value if we are time synchronous
      if hold then setPwm(curr_profile, curr_setpoint, curr_error, next_output);
      -- calculate the new pwm control value
      if feedback
      then next_output := limit(calcPID(measured_value))
      else next_output := limit(getSetpoint());
      -- output the new value directly if we are not time synchronous
      -- otherwise wait until the next period is due
      if not hold then setPwm(curr_profile, curr_setpoint, curr_error, next_output) )

thread
    -- run the controller at 1 kHz and assume no jitter
    periodic (0.001, 0, 0, 0.001)(CtrlLoop)
    
```

# Printer paper path - case study (4)

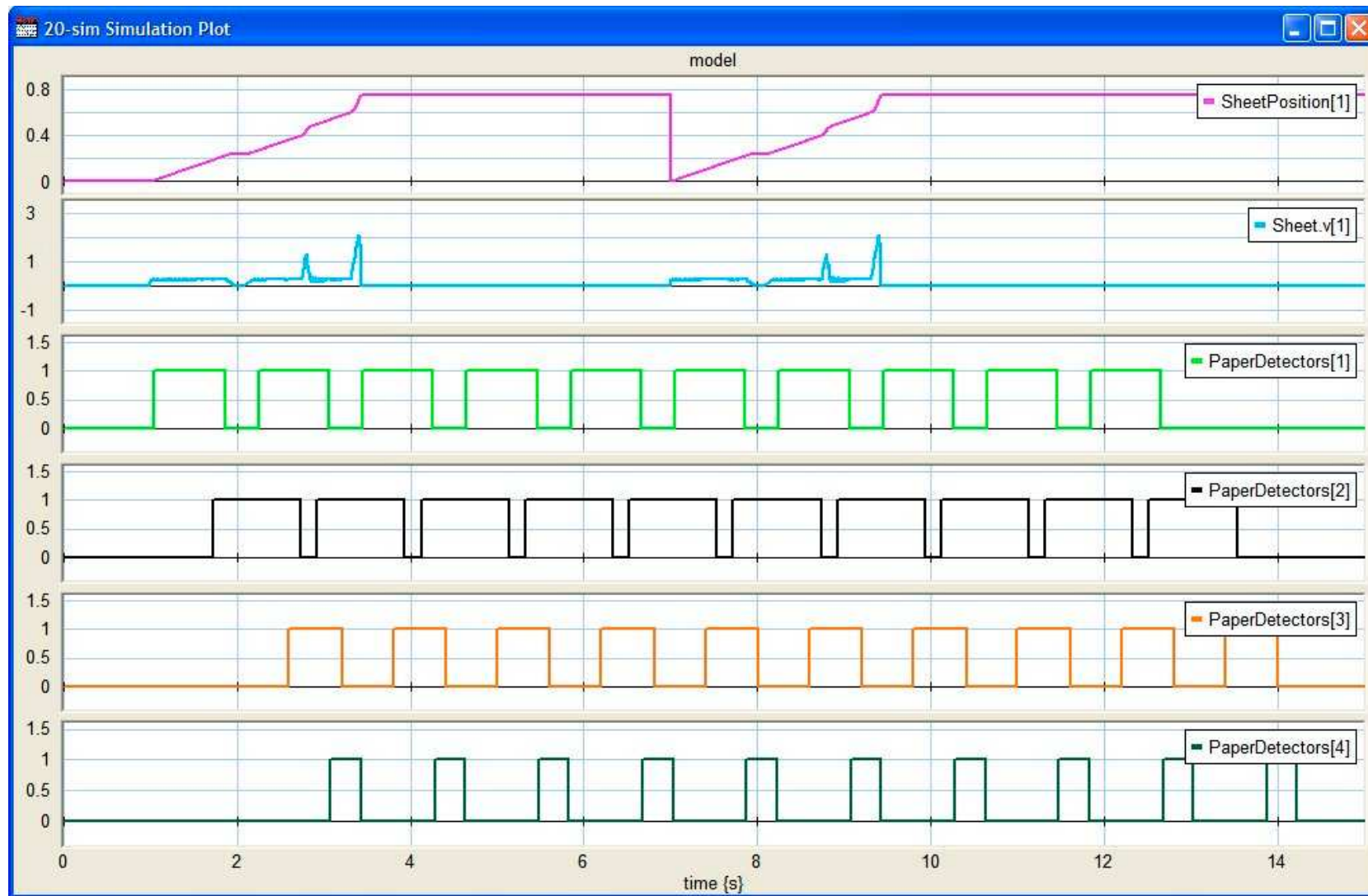


# Printer paper path - case study (5)





# Printer paper path - case study (6)



# Printer paper path - case study (7)

